

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Dae-Woong KANG, et al.

Serial No.: 10/758,802 Examiner: Warren, Matthew E.

Filed: January 15, 2004 Group Art Unit: 2815

Confirmation No.: 6500

For: METHODS OF FABRICATING A SEMICONDUCTOR DEVICE  
HAVING MULTI-GATE INSULATION LAYERS AND  
SEMICONDUCTOR DEVICES FABRICATED THEREBY

Date: November 8, 2006

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT**

Responsive to the Office Action, Paper No. 20060806, dated August 10, 2006, please amend the application as follows.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks** begin on page 6 of this paper.